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JUL 25 2005  
PATENT & TRADEMARK OFFICE

Docket No.: YHK-0112

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:

Joong Seo PARK

Serial No.: 10/743,284

Filed: 12/23/2003

Confirmation No.: 7858

Group Art Unit: 2821

Examiner: A. Minh D.

**Customer No.: 34610**

For: ENERGY RECOVERING APPARATUS AND METHOD FOR PLASMA DISPLAY PANEL

**REQUEST FOR RECONSIDERATION**

U.S. Patent and Trademark Office  
Customer Window, Mail Stop Amendment  
Randolph Building  
401 Dulany Street  
Alexandria, Virginia 22314

Sir:

In reply to the Office Action dated January 25, 2005, the period for reply being extended from April 25, 2005 to July 25, 2005 by a Petition for a Three-Month Extension of Time filed herewith, reconsideration of the Office Action is respectfully requested.

Concurrently filed with this reply is an Information Disclosure Statement. Further, it is noted that U.S. Application Nos. 10/416,286, 10/824,417, and 10/743,289 are related applications, and all references cited during the prosecution of those applications should be considered during the prosecution of this application.

The indication of allowable subject matter in claims 4-11 is appreciated and acknowledged. However, it is respectfully submitted that rejected claims 1-3 and 12-15 are distinguishable over the cited reference. In particular, claims 1-3, and 12-15 stand rejected under

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35 U.S.C. §102(b) over Lee et al. (U.S. Patent No. 6,680,581). This rejection is respectfully traversed.

Lee et al. cannot disclose all the claimed features and the combination thereof, as required under §102. For example, Lee et al. fails to disclose or teach “a second path, being separated from the source capacitor, for supplying energy of the inductor to the plasma display panel” and the combination thereof, as recited in independent claim 1 and “applying energy of the inductor to the panel using a second path that is separated from the source capacitor and includes the inductor and the plasma display panel,” and the combination thereof, as recited in independent claim 12.

Independent claims 1 and 12 broadly recites the features illustrated in the disclosed embodiment of the specification. In a non-limiting example to the claims, the specification discloses that during the time period T2 of Figures 5, the capacitor CS is separated from the current path, as shown in Figure 7. However, the prior art Figures 1A and 1B of Lee et al. do not disclose such a path.

Submitted herewith is a full detailed text of U.S. Patent Nos. 4,866,349 and 5,081,400 to Weber et al., which corresponds to Figures 1A and 1B of Lee et al. The details of Figures 1A and 1B of Lee et al. can be gathered from these two documents. As illustrated in Figure 1B of Lee et al., during mode 1, the transistor S1 is turned on. Hence, when the energy of the inductor is supplied to the plasma display panel, the switch S1 is turned on, and the path is not separated

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from the capacitor Cc, as illustrated in Figure 1A. In fact, there are no two paths in Figure 1A and Figure 1B of the Lee et al. since a single path is used for supplying energy of the inductor to the plasma display panel. Hence, Lee et al. can not disclose or teach the claimed features and the combination thereof of independent claims 1 and 12.

Lee et al. does not disclose all the claim features and the combination thereof, as required under §102. Hence, withdrawal of this §102 rejection is respectfully requested.

In view of the foregoing amendments and remarks, it is respectfully submitted that this application is in condition for allowance. Favorable consideration and prompt allowance are earnestly solicited.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this, concurrent and future replies, including extension of time fees, to Deposit Account 16-0607 and please credit any excess fees to such deposit account.

Respectfully submitted,  
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